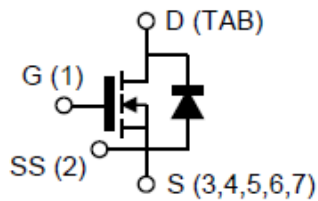
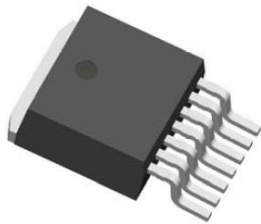


## Silicon Carbide Power MOSFET (N-Channel Enhancement)

$V_{DS}$	1700V
$I_D$ (25°C)	7.7A
$R_{DS(on)}$	750mΩ



### Features

- High speed switching
- Essentially no switching losses
- Reduction of heat sink requirements
- Maximum working temperature at 175 °C
- High blocking voltage
- Fast Intrinsic diode with low recovery current
- High-frequency operation
- Halogen free, RoHS compliant

### Typical Applications

Typical applications are in power factor correction(PFC), solar inverter, uninterruptible power supply, motor drives, photovoltaic inverter, electric car and charger.

### Mechanical Data

- **Package:** TO-263-7L
- **Terminals:** Tin plated leads
- **Polarity:** As marked

### ■Maximum Ratings ( $T_c=25^\circ\text{C}$ Unless otherwise specified)

PARAMETER	SYMBOL	UNIT	VALUE	TEST CONDITIONS	NOTE
Device marking code			D2170750B7GH		
Drain source voltage @ $T_j=25^\circ\text{C}$	$V_{DS,max}$	V	1700	$V_{GS}=0\text{ V}, I_D=100\mu\text{A}$	
Gate source voltage @ $T_j=25^\circ\text{C}$	$V_{GS,max}$	V	-10/+25	Absolute maximum values (AC $f > 1\text{Hz}$ , duty cycle $< 1\%$ )	Note1
Gate source voltage @ $T_j=25^\circ\text{C}$	$V_{GS,op}$	V	-5/+20	Recommended operational values	
Continuous drain current @ $T_c=25^\circ\text{C}$	$I_D$	A	7.7	$V_{GS}=18\text{V}, T_c=25^\circ\text{C}$	Fig.14
Continuous drain current @ $T_c=110^\circ\text{C}$			5.3	$V_{GS}=18\text{V}, T_c=110^\circ\text{C}$	
Pulse Drain Current	$I_{D,pulse}$	A	22	Limited by $t_{pw}$	Fig.15
Power Dissipation	$P_{TOT}$	W	89	$T_c=25^\circ\text{C}, T_j = 175^\circ\text{C}$	Fig.13
Avalanche energy, Single Pulse	$E_{AS}$	mJ	80	$V_{DD}=75\text{V}, L=25\text{mH}$	
Operating junction and Storage temperature range	$T_j, T_{stg}$	$^\circ\text{C}$	-55 to +175		
Soldering temperature	$T_L$	$^\circ\text{C}$	260	1.6mm (0.063") from case for 10s	



### ■Static Electrical Characteristics (Tc=25°C unless otherwise specified)

PARAMETER	SYMBOL	UNIT	Min.	Typ.	Max.	Test Conditions	Note
Gate threshold voltage	$V_{GS(th)}$	V	1.5	3.1	4.5	$V_{DS}=V_{GS}$ , $I_D=1mA$	Fig.4, 11
Drain source breakdown voltage	$V_{(BR)DSS}$	V	1700			$V_{GS}=0$ , $I_D=100\mu A$	
Zero gate voltage drain current	$I_{DSS}$	$\mu A$		<1	100	$V_{DS}=1700V$ , $V_{GS}=0V$	
				5	500	$V_{DS}=1700V$ , $V_{GS}=0V$ , $T_J=175^\circ C$	
Gate source leakage current	$I_{GSS}$	nA			250	$V_{GS}=18V$ , $V_{DS}=0V$	
Current drain source on-state resistance	$R_{DS(on)}$	$m\Omega$		530	750	$V_{GS}=18V$ , $I_D=2A$	Fig.3, 5, 6
				1200		$V_{GS}=18V$ , $I_D=2A$ , $T_J=175^\circ C$	
Transconductance	$g_f$	S		2.0		$V_{DS}=10V$ , $I_D=2A$	

### ■Dynamic Electrical Characteristics (Tc=25°C unless otherwise specified)

PARAMETER	SYMBOL	UNIT	Min.	Typ.	Max.	Test Conditions	Note
Input capacitance	$C_{iss}$	$pF$		490		$V_{DS}=1000V$ , $V_{GS}=0V$ , $T_J=25^\circ C$ , $f=1MHz$ , $V_{AC}=25mV$	Fig.10
Output capacitance	$C_{oss}$			17			
Reverse capacitance	$C_{rss}$			4			
Coss stored energy	$E_{oss}$	$\mu J$		11			Fig.12
Gate source charge	$Q_{gs}$	$nC$		6		$V_{DS}=1000V$ , $V_{GS}=-4/18V$ , $I_D=2A$	Fig.16
Gate drain charge	$Q_{gd}$			22			
Gate charge	$Q_g$			36			
Internal Gate Resistance	$R_{G(int)}$	$\Omega$		8.0	11.0	$f=1MHz$ , $V_{AC}=25mV$	

### ■Switching Characteristics (Tc=25°C unless otherwise specified)

PARAMETER	SYMBOL	UNIT	Min.	Typ.	Max.	Test Conditions	Note
Turn on delay time	$t_{d(on)}$	$ns$		8		$V_{DD}=1200V$ , $V_{GS}=-4/+18V$ , $I_D=2A$ , $L=300\mu H$ , $R_{G(ext)}=2.7\Omega$	Fig.17, 18
Rise time	$t_r$			19			
Turn off delay time	$t_{d(off)}$			16			
Fall time	$t_f$			80			
Turn on switching energy	$E_{on}$	$\mu J$		88			
Turn off switching energy	$E_{off}$			18			



■Body diode characteristics (Tc=25°C unless otherwise specified)

PARAMETER	SYMBOL	UNIT	Min.	Typ.	Max.	Test Conditions	Note
Diode forward voltage	V <sub>SD</sub>	V		4.3		V <sub>GS</sub> =0V, I <sub>SD</sub> =2A	Fig.8
Continuous diode forward current	I <sub>S</sub>	A		10.7		V <sub>GS</sub> =0V, Tc=25°C	
Reverse recovery time	t <sub>rr</sub>	nS		36		V <sub>DS</sub> =1200V, V <sub>GS</sub> =0V, I <sub>SD</sub> =2A, di/dt=300A/uS	
Reverse recovery charge	Q <sub>rr</sub>	nC		70			
Peak reverse recovery current	I <sub>rrm</sub>	A		3.8			

Note 1: When using SiC Body Diode the maximum recommended V<sub>GS</sub> = -5V

■Thermal Characteristics (T<sub>a</sub>=25°C Unless otherwise specified)

PARAMETER	SYMBOL	UNIT	Value
Thermal resistance	R <sub>θJ-C</sub>	°C/W	1.69

■Typical Characteristics

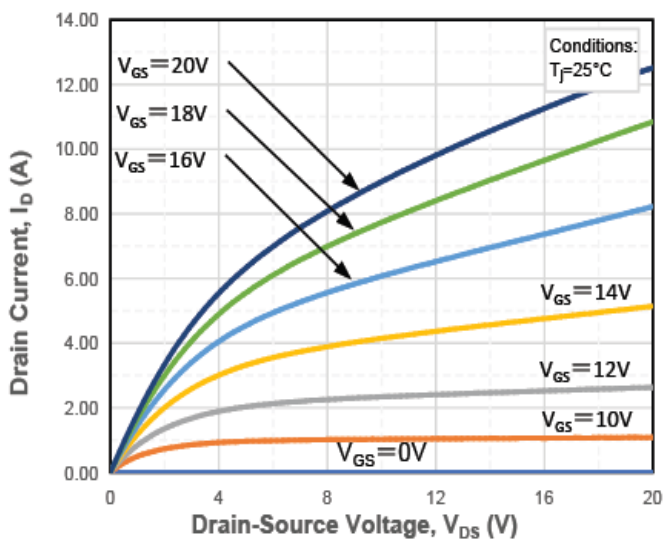


Figure 1. Output Characteristics Tj = 25°C

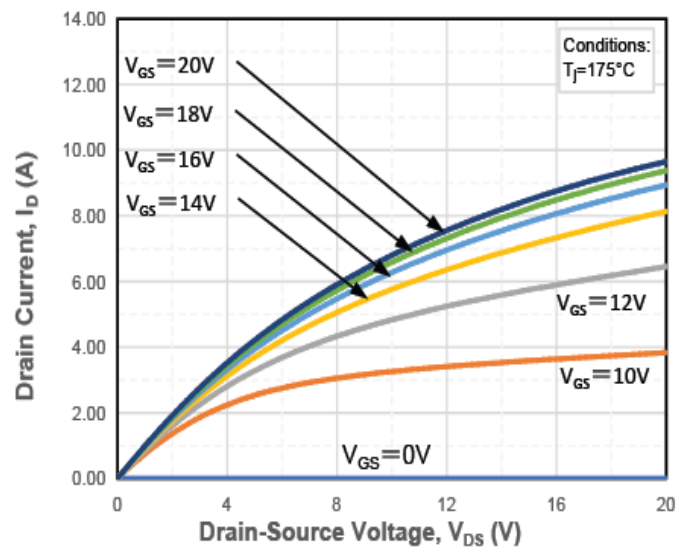


Figure2. Output Characteristics Tj = 175°C

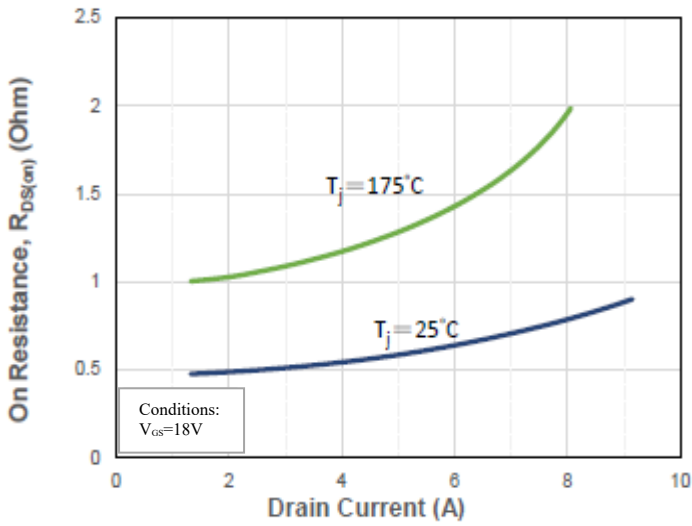


Figure 3. On-resistance vs. drain current

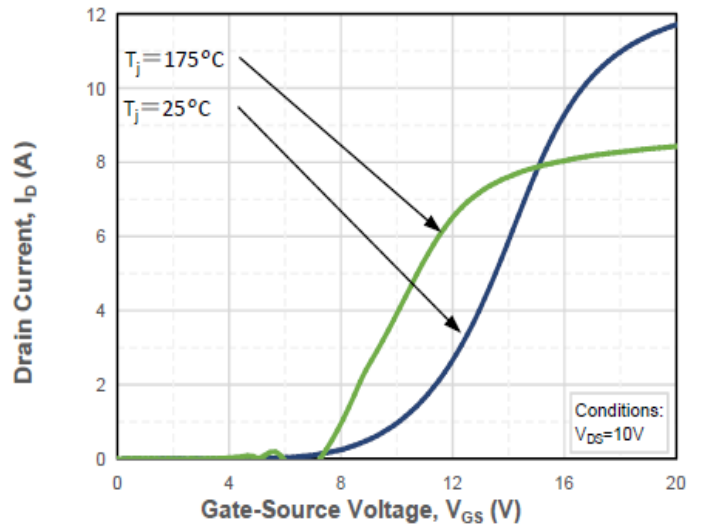


Figure 4. Transfer Characteristics for various Tj

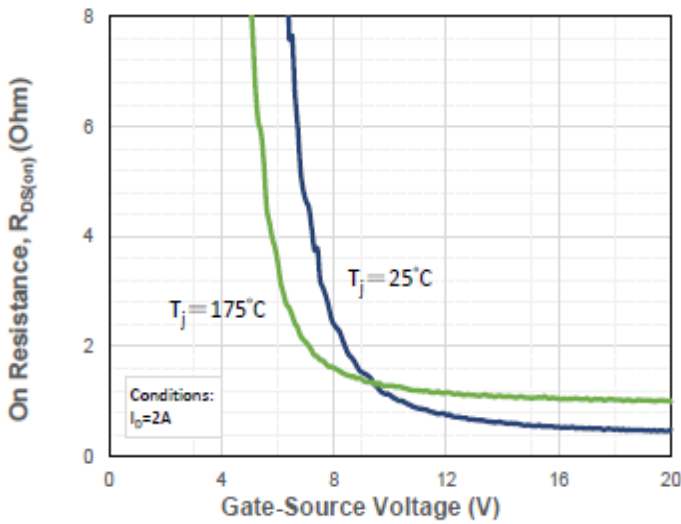


Figure 5. On-resistance vs. gate voltage for various Tj

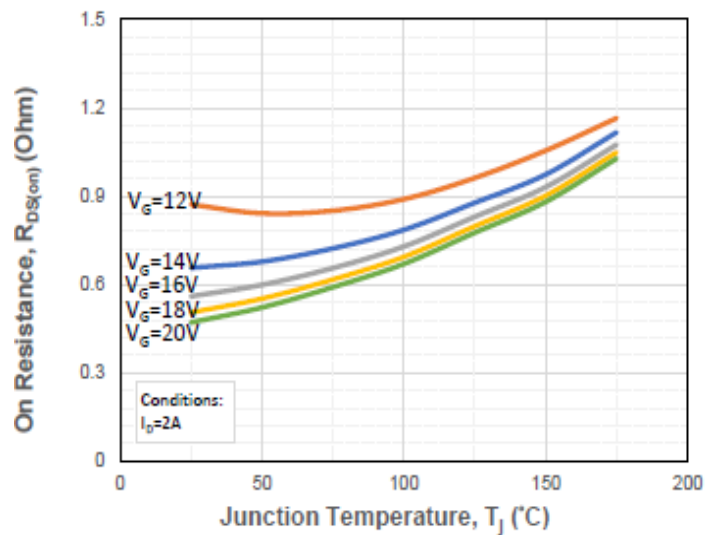


Figure 6. On-resistance vs. Temperature for various Gate voltage

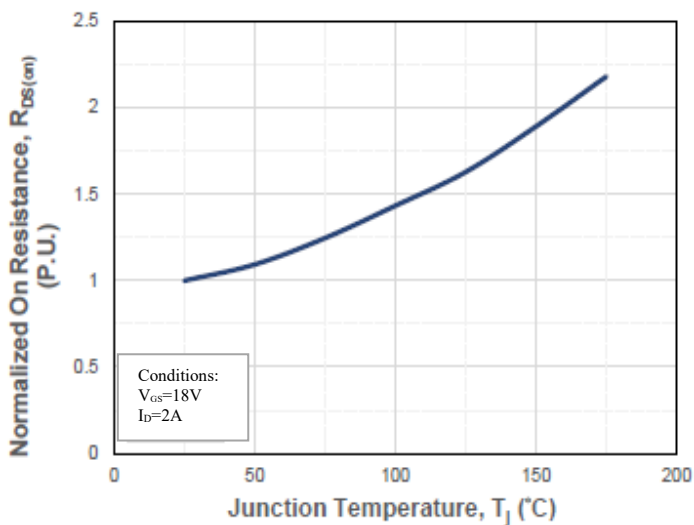


Figure 7. Normalized On-Resistance vs. Temperature

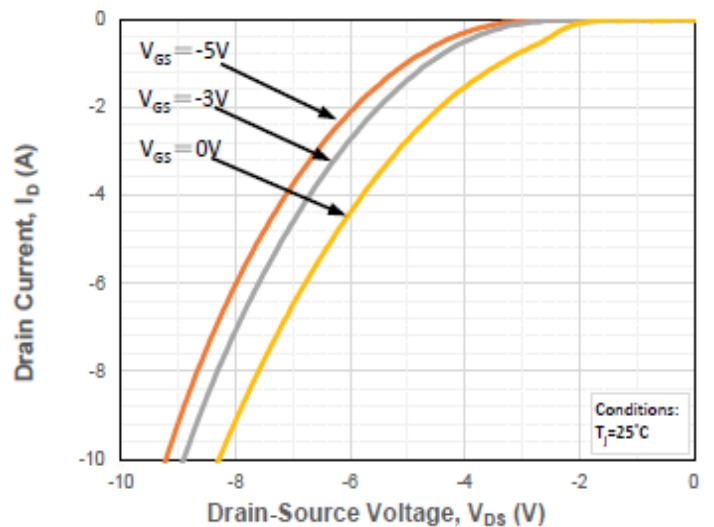


Figure 8. Body Diode Characteristics at Tj = 25°C

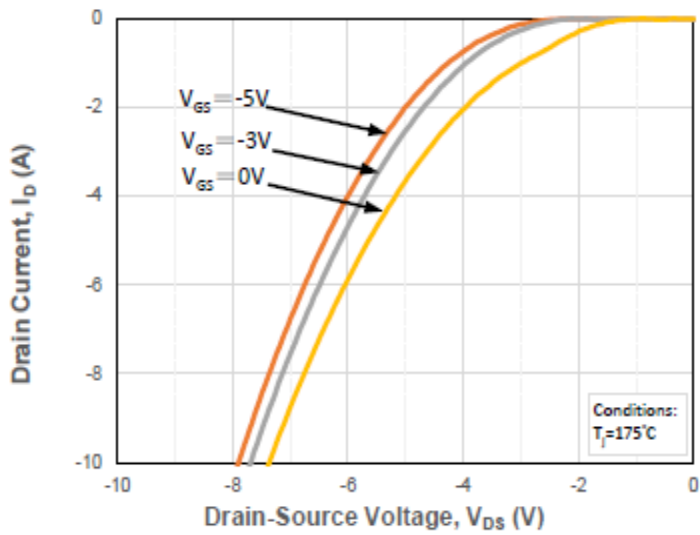


Figure 9. Body Diode Characteristics at  $T_j = 175^\circ\text{C}$

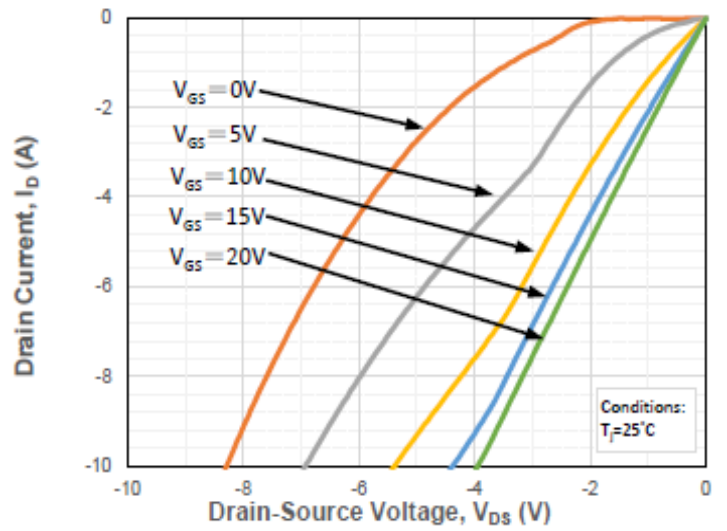


Figure 10. 3rd Quadrant Characteristics at  $T_j = 25^\circ\text{C}$

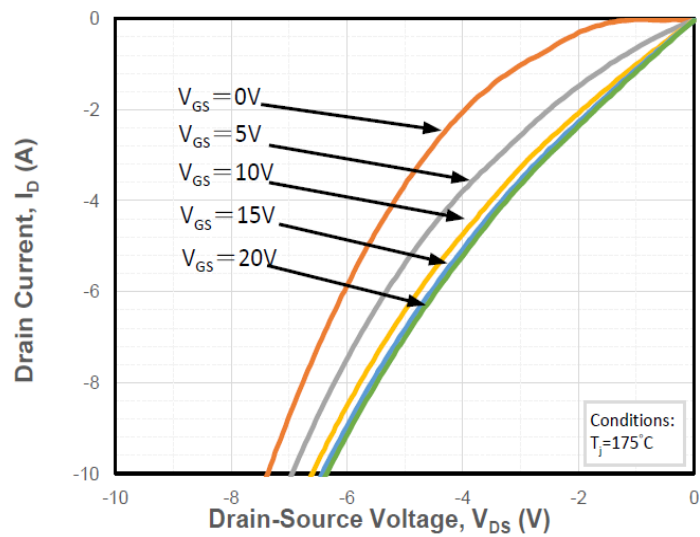


Figure 11. 3rd Quadrant Characteristics at  $T_j = 175^\circ\text{C}$

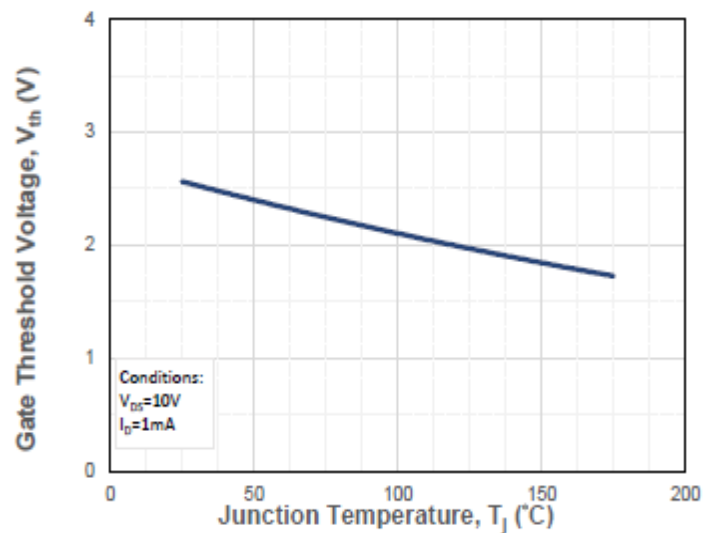


Figure 12. Threshold Voltage vs. Temperature

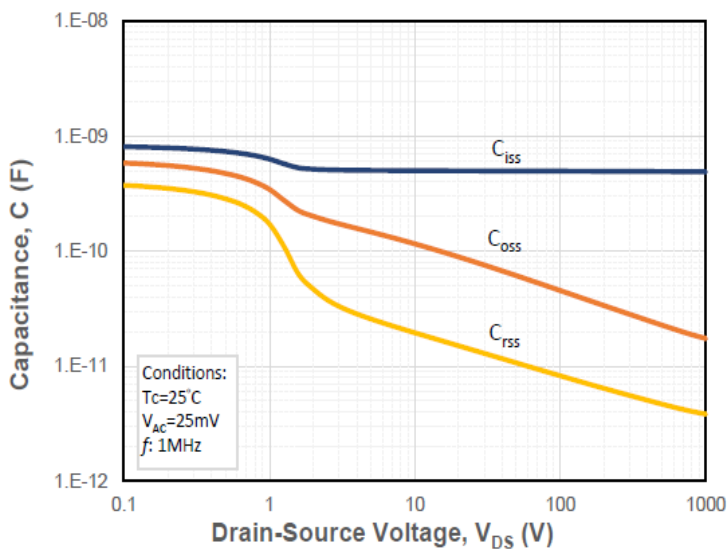


Figure 13. Capacitances vs. Drain to Source Voltage

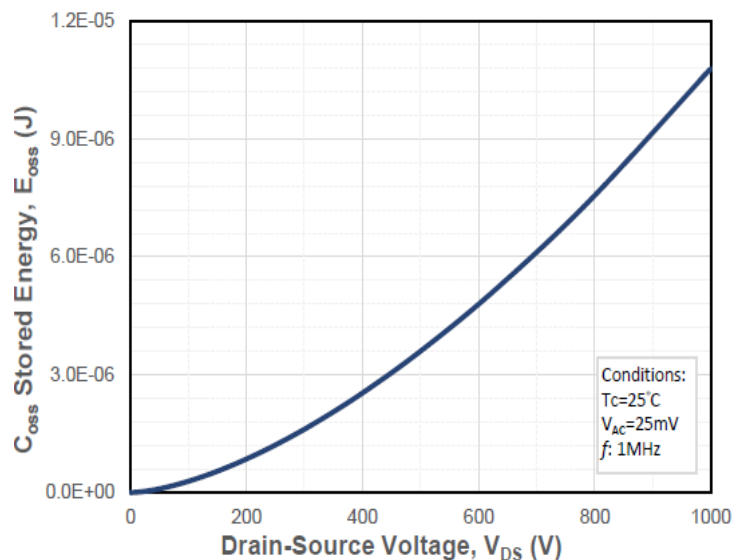


Figure 14. Output Capacitor Stored Energy

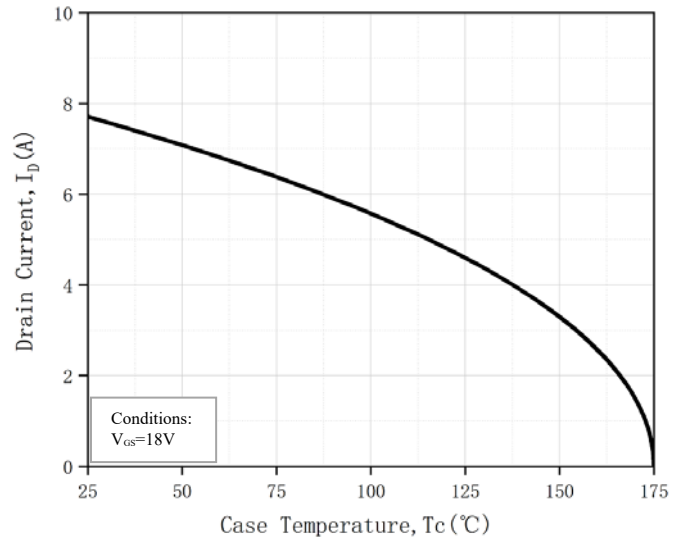
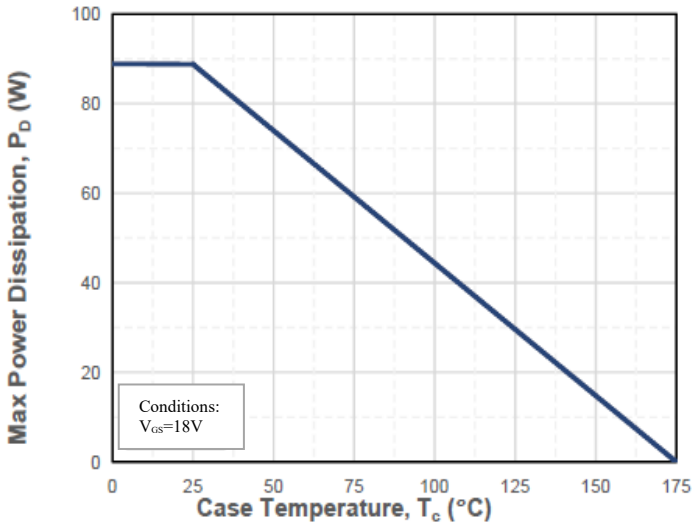


Figure 15. Maximum Power Dissipation Derating vs. Case Temperature

Figure 16. Drain Current Derating vs. Case Temperature

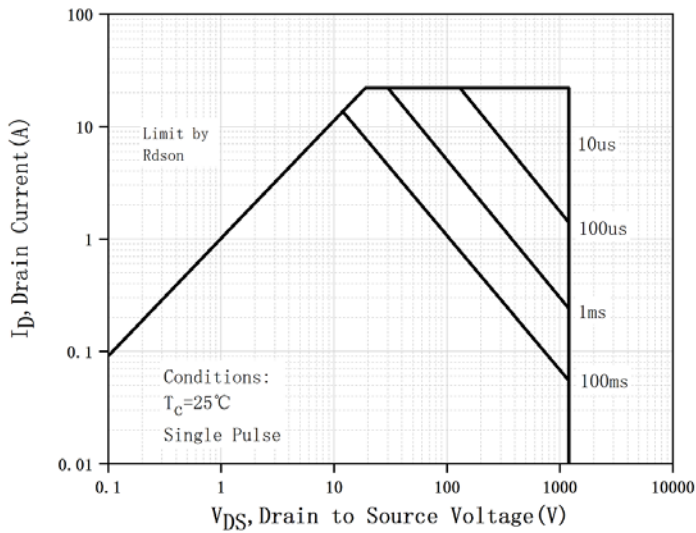


Figure 17. Safe Operating Area

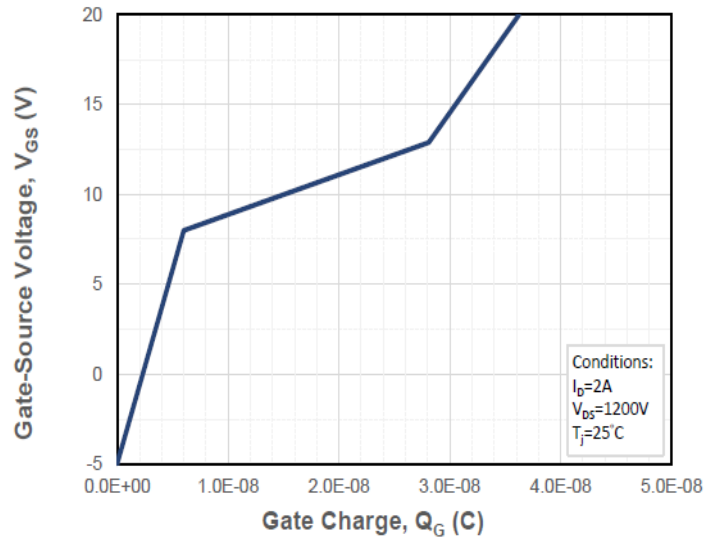


Figure 18. Gate Charge Characteristics

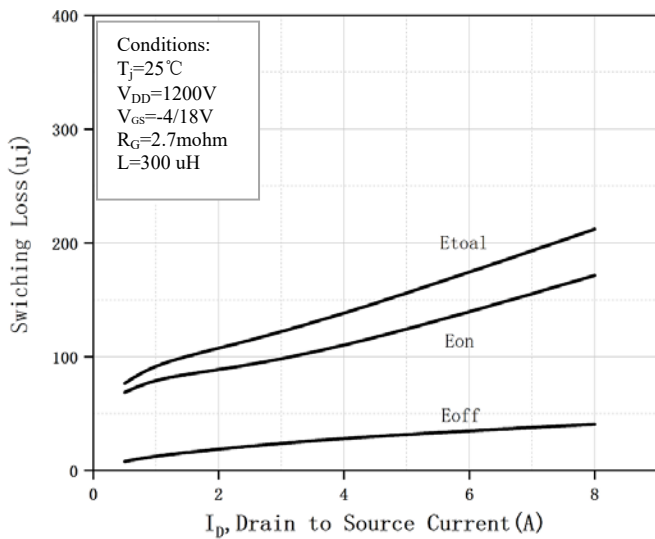


Figure 19. Clamped Inductive Switching Energy vs. Drain Current

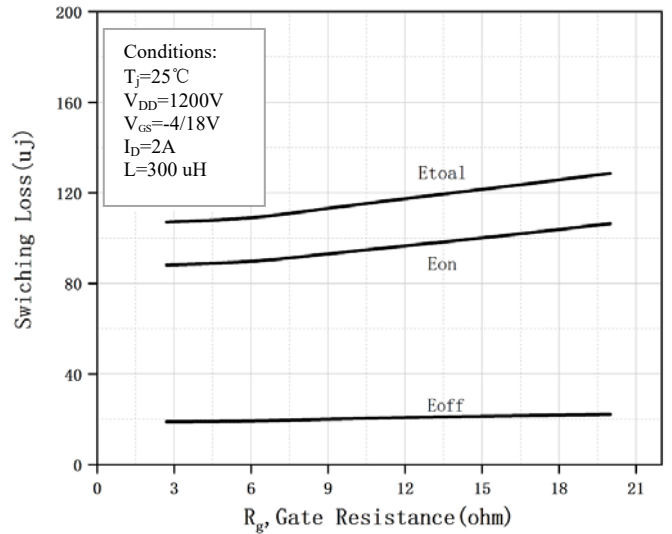


Figure 20. Clamped Inductive Switching Energy vs. External Gate Resistor ( $R_G(ext.)$ )

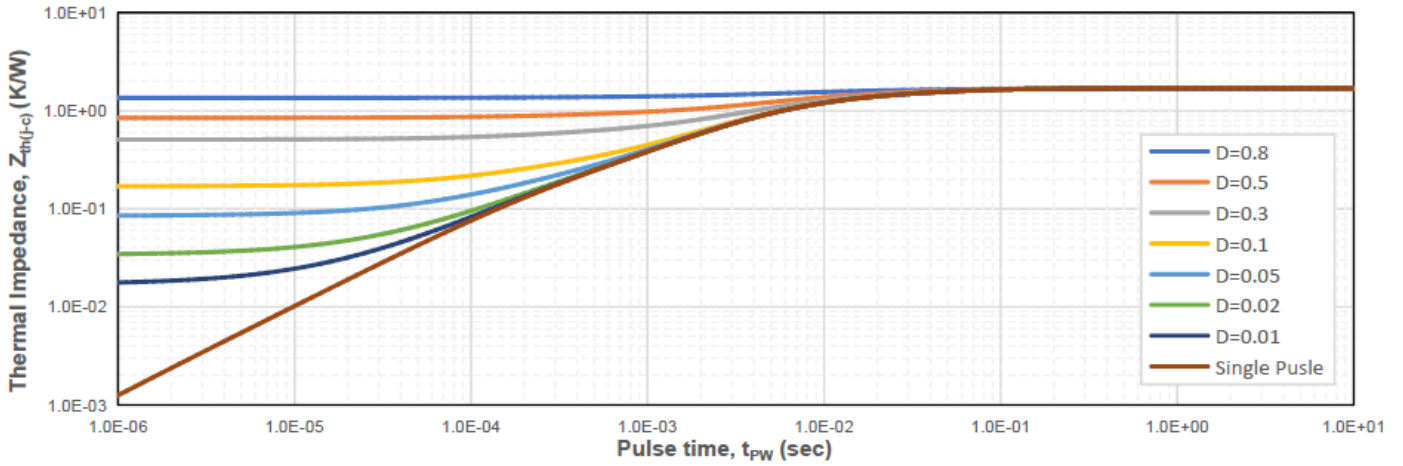


Figure 21. Transient Junction to Case Thermal Impedance

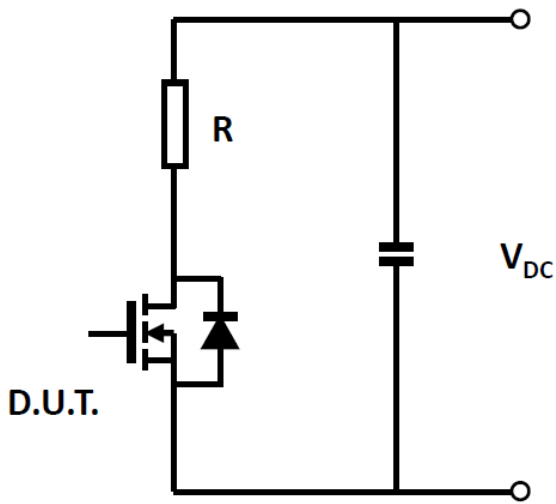


Figure 22. Schematic of Resistive Switching

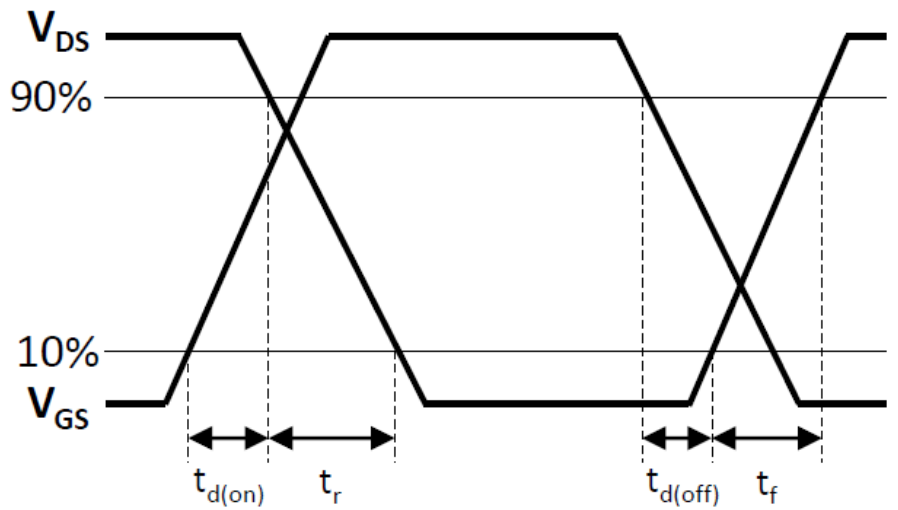
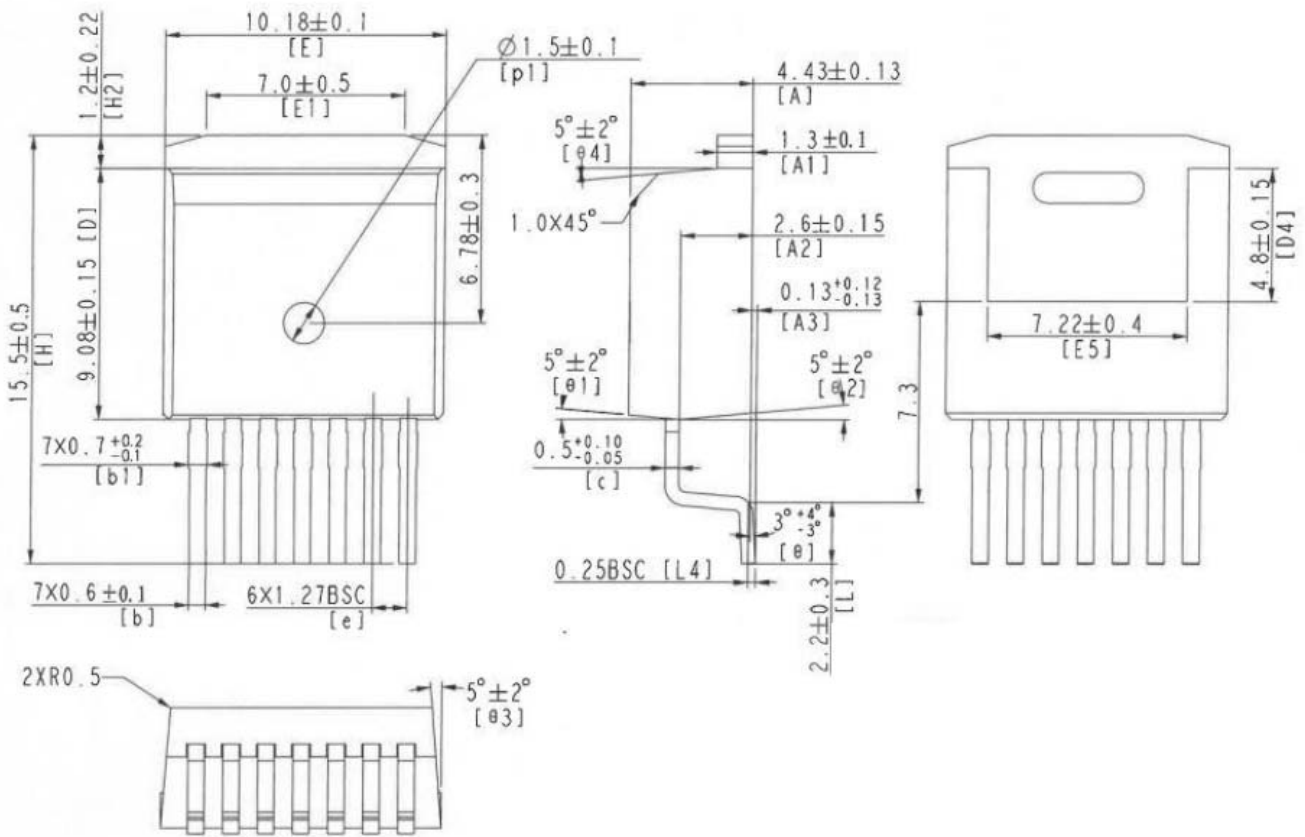


Figure 23. Switching Times Definition





■Outline Dimensions







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